



Miss. Madhuri Sonkhaskar

M.Tech (Electronics)

[madhuri.sonkhaskar skncoe](mailto:madhuri.sonkhaskar@skncoe.edu)

@sinhgad.edu

Mob. No.: 9503421557

Academic Qualification:

Degree	Institute Name	University Name	Year of Passing	Qualification Status
M. Tech (Electronics)	SGGSIE&T, Nanded	Shri Ramanand Teerth Marathwada Uni., Nanded	2011	Completed
B.E.(E& TC)	B. N. College Of Engg. Pusad	Sant Gadge Baba University Amravati	2000	Completed

Training Details

Training Title	Arrange By	Venue	Date	Level	Category	Type
MATLAB &SIMULINK	INDIASOFT	KITS, RAMTEK	27 th to 29 th December 2006	State		
Faculty Induction Training	SPPU, Pune	GHRIET	25-26 July 2012	State		
MATLAB for ALL	SVMIT	SVMIT, Bangalore	04-08 June 2020	National		

Workshop Attended

Workshop	Topic	Conducted By	Date	Level	Category	Type
Seminar	Avenues in computing, communication and Information Technology (ACCIT-05)"	K.I.T.S., Ramtek.	12-13th March 2005	National	Institution of Engineers	-
FDP	Modern Communication Systems	SSGMCOE, Shegaon, Dist. Buldhana	18 th to 22 nd June 2007	National	AICTE	-
QIP-Short	Digital	IIT Bombay	01- 05 June	National	QIP	-

term course	Image Processing for Remote Sensing		2009			
Workshop	Texture Analysis and Applications	SGGSIET, Nanded	10th November 2009	National	-	-
Workshop	Effective Research Methodology	SSSGIET, Nanded	20th March 2010	State	-	-
STTP	Contemporary & Futuristic Mobile & Wireless Technologies	GHREM, Pune	21- 25th November 2011	National(ISTE Approved)	ISTE Chapter	-
Workshop	Advanced Digital Signal Processing	GHRIET, Pune	17-19 July 2010	State	In Coll. IIT Kharagpur	-
Workshop	Teaching Methodology for the Subject Ele. IV)	VIIT, Pune	7th January 2012	BOS, Pune	-	-
Seminar	Applications VLSI in DSP	GHRIET, Pune	15- 17 March 2012	National	-	-
FDP	Embedded image Processing	SCOE, Pune	6-10 May 2013	IEEE	IEEE Pune section	-
FDP	RF System & Design	SCOE, Pune	7-11 December 2015	State	-	-
FDP	Advances in Wireless and Mobile Technologies .	SCOE, Pune	7-11 June 2016	State	In Association with Tektronix-Keithley	-
Workshop	Advanced Telecommunication Symposium	SCOE, Pune	27th Feb 2017	State (National Instruments)		-
FDP	Image Processing – Advances & Research Challenges	SCOE, Pune	11-15 December 2017	National	IEEE & Math Works	-
Workshop	Video Processing: Opportunities and Challenges	SKNCOE, Pune	29th January 2018	State	-	-
STTP	Green Internet of Things	VESIT, Mumbai	2-8 January 2019	State	ISTE Approved	
Workshop	Advanced Concepts in circuit simulation & compact	RCOEM, Nagpur	25-26 May 2020	State	Association with IEEE	-

	Modeling					
FDP	Managing Online classes and co creating MOOCS 2.0	Ramanujan College(University of Delhi)	May 18- June 03, 2020	Govt. Of India	MHRD	-
STTP	Electric vehicle: Technologies, Challenges and opportunities for Integration and Renewable Resources in INDIA	RMK Engg College	7-12 Dec 2020	National	AICTE	-
FDP	Research perspectives in Machine intelligence and computing	SAIRAM Institute of Technology	27-31 Oct 2020	National	-	-
FDP	Future Technologies in Cloud Computing: AWS, Sales force, Azure	ZCER, Pune	22-26 Feb 22	State	ISTE Approved	-

Conference Details

Name	Venue	Date	Level	Category	Sponsored By
Future trends in Engineering and Technology	Government Polytechnic, Amravati	17th March 2001	State Level	-	Institute of Engineers
Development of Test bench for Acquisition Module	SCOE, Pune	23-24 Oct 2013	National (NCLTE-2013)	-	QIP
PAPR Reduction Techniques Focusing on Harmony Algorithm	ICRIEST, Pune	1st Feb 2015	International	IRAJ Research Forum	Institute of Research & Journals.
Test bench development for acquisition module FPGA of Ultrasonic Flow meter.	ICPC 2015	8-10 JAN 2015	IEEE	Advanced Communication technology and application for society	-
PAPR Reduction Techniques Focusing on Harmony Search Algorithm	International Journal of Industrial Electronics and Electrical Engineering	March 2015	IJIEEE	ISSN: 2347-6982	-

Research Project

Project name	Description	Status	Budget	Project type
NA				

Research Paper & Journal

ISSN/ISBN No	Paper Title	Publis h Date	Journal/Confe rence Name	Level	Paper Status	Authors
ISSN-2347-6982	PAPR REDUCTION TECHNIQUES FOCUSING ON HARMONY SEARCH ALGORITHM	March - 2015	International Journal of Industrial Electronics and Electrical Engineering	Journal Paper	Published	Madhuri Sonkhaskar
ISBN: 978-1-4799-6272-3	Test bench development for acquisition module FPGA of Ultrasonic flow meter	April 2015	IEEE	IEEE Xplore	Published	M.M Sonkhaskar

Membership Details

Membership Body		Details	Duration	Level
ISTE	-	-	1 Year	National

Declaration

I hereby declare that all the above information furnished by me are true to the best of my knowledge.

Date: 13th June 2022

Name: Madhuri Sonkhaskar